





Combining Truncated Binary Search Tree and Direct Search for Flexible Piecewise Function Evaluation for Explicit MPC in Embedded Microcontrollers

By:

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- Piecewise Function Evaluation Problem.
- Main Motivation: Explicit Model Predictive Control (eMPC) leads to piecewise affine state feedback solutions which may consist of 10000's affine function pieces. With fast evaluation, eMPC can be applied to mechatronic control systems.
- Approach 1: Orthogonal Truncated Binary Search Tree combined with direct search. Zanjan University Zanjan University Zanjan University Zanjan University Zanjan University Zanjan University
- Approach 2: Orthogonal Truncated Binary Search Tree combined with a simple approximation method to replace the direct search phase.
- Examples & Conclusions.





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Piecewise Function Evaluation Problem

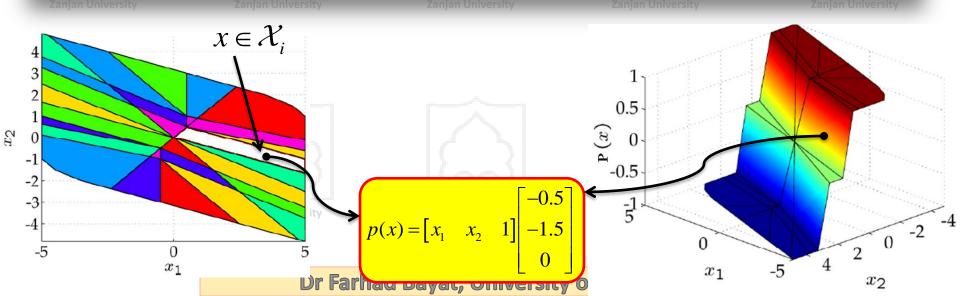
Definition:

Let a compact set $X \subset \mathbb{R}^n$ be partitioned into a set of N_r convex polyhedral regions \mathcal{X}_i , $i = 1, ..., N_r$ so that $X = \bigcup_{i=1}^{N_r} \mathcal{X}_i$.

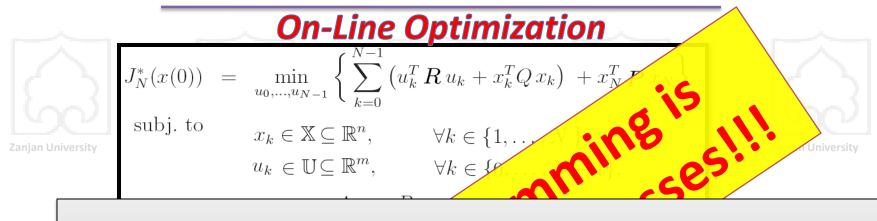
(i) A piecewise function $p(x): X \to R$ is defined as $p(x) = f(x | \phi_i) = f_i(x), \forall x \in \mathcal{X}_i$.

(ii) p(x) is said to be a Piecewise Affine (PWA) function if $f_i(x) = [x^T 1]\phi_i$ and continuous if $f_i(x) = f_j(x), \forall x \in \mathcal{X}_i \cap \mathcal{X}_j$.

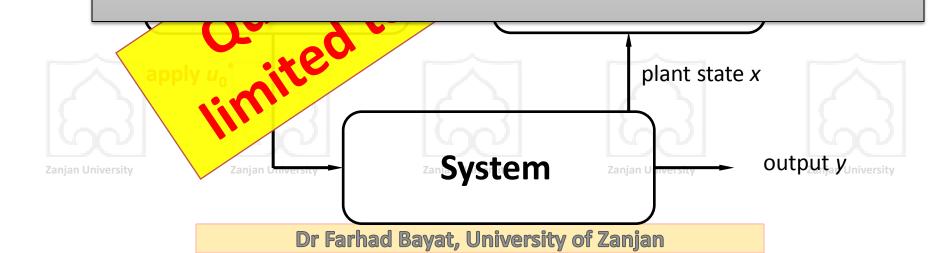
(iii) For a given query point $x \in X$, the <u>PWA function evaluation problem</u> is referred to find $p(x) = f_i(x), i \in \{1, ..., N_r\}$ for which $x \in \mathcal{X}_i$.



Main Motivation: Model Predictive Control







Explicit Solution of MPC

$$J_{N}^{*}(x(t)) = \min_{u_{t,...,u_{t+N-1}}} \left\{ \sum_{k=0}^{N-1} (u_{t+k}^{T} R u_{t+k} + x_{t+k}^{T} Q x_{t+k}) + x_{t+N}^{T} P x_{t+N} \right\}$$
subj. to
$$x_{t+k} \in \mathbb{X} \subseteq \mathbb{R}^{n}, \quad \forall k \in \{1, ..., N\},$$

$$u_{t+k} \in \mathbb{U} \subseteq \mathbb{R}^{m}, \quad \forall k \in \{0, ..., N-1\},$$

$$x_{t+k+1} = A x_{t+k} + B u_{t+k}, \ x_{t} = x(t),$$

$$Q = Q' \succeq 0, \ R = R' \succ 0, \ P \succeq 0$$
(1)
$$substituting \quad x_{t+k} = A^{k} x(t) + \sum_{j=0}^{k-1} A^{j} B u_{t+k-1-j},$$

$$J_{N}^{*}(x_{t}) = x_{t}' Y x_{t} + \min_{U_{N}} \{\frac{1}{2} U_{N}' H U_{N} + x_{t}' F x_{t}\}$$
subject to:
$$GU_{N} \leq W + E x_{t},$$

$$H \succ 0,$$

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Optimization Variable: $U_N = [u_t^T, u_{t+1}^T, \dots, u_{t+N-1}^T]^T$

Multi-Parametric Quadratic Programming: Explicit Solution of MPC

(Bemporad et. al, 2002a):

Theorem. Consider the multi-parametric quadratic program (2) and let H>0and X convex. Then the set of feasible parameters $X_f \subseteq X$ is convex, the optimizer $z(x): X \to R^s$ is continuous and piecewise affine.

Corollary: The control law $u(t) = \begin{bmatrix} I & 0 & \dots & 0 \end{bmatrix} U_N^*$ defined by the optimization problem (1) is continuous and piece-wise affine:

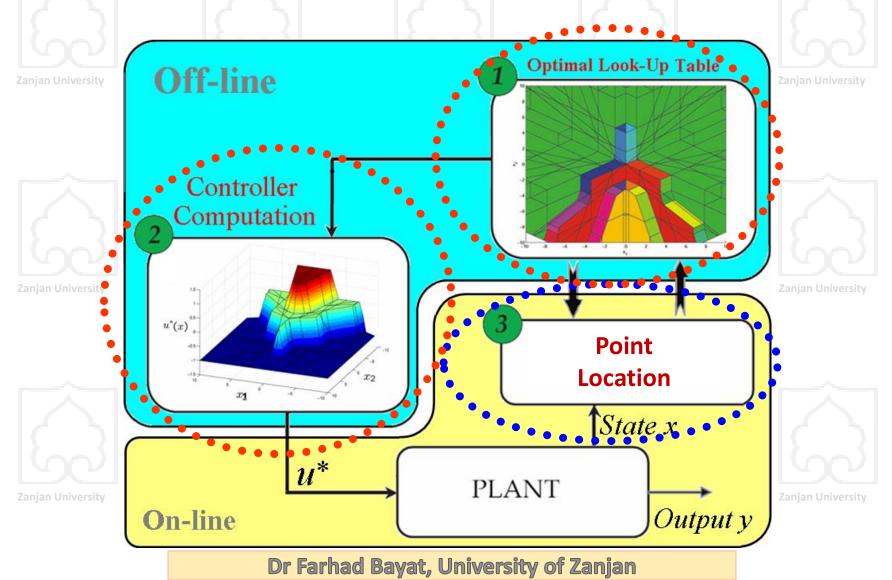
$$u(x(t)) = F_i x(t) + G_i, \quad \forall x \in \mathcal{X}_i$$
$$\mathcal{X}_i = \left\{ x \in \mathbb{R}^n \mid H_i x \le K_i \right\}$$

Switching between polyhedral regions occur when the optimal active set changes

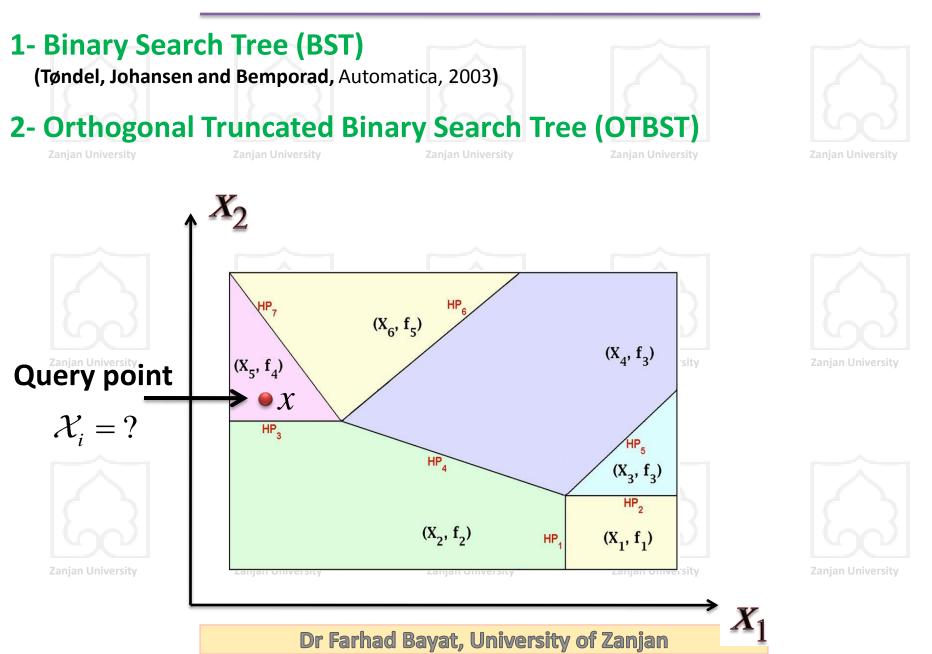
Dr Farhad Bayat, University of Language

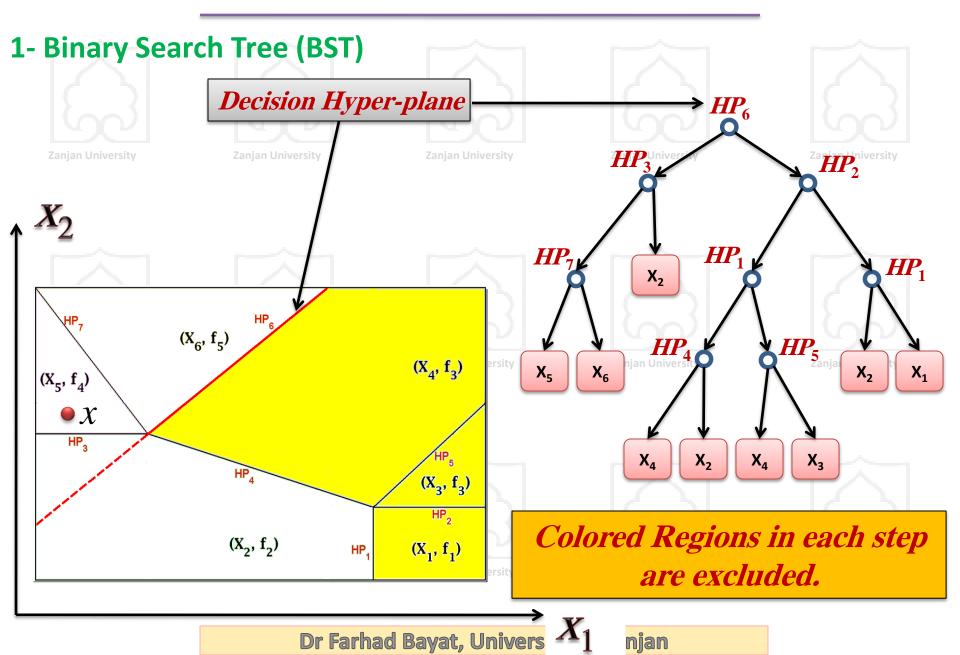
Explicit Solution of MPC

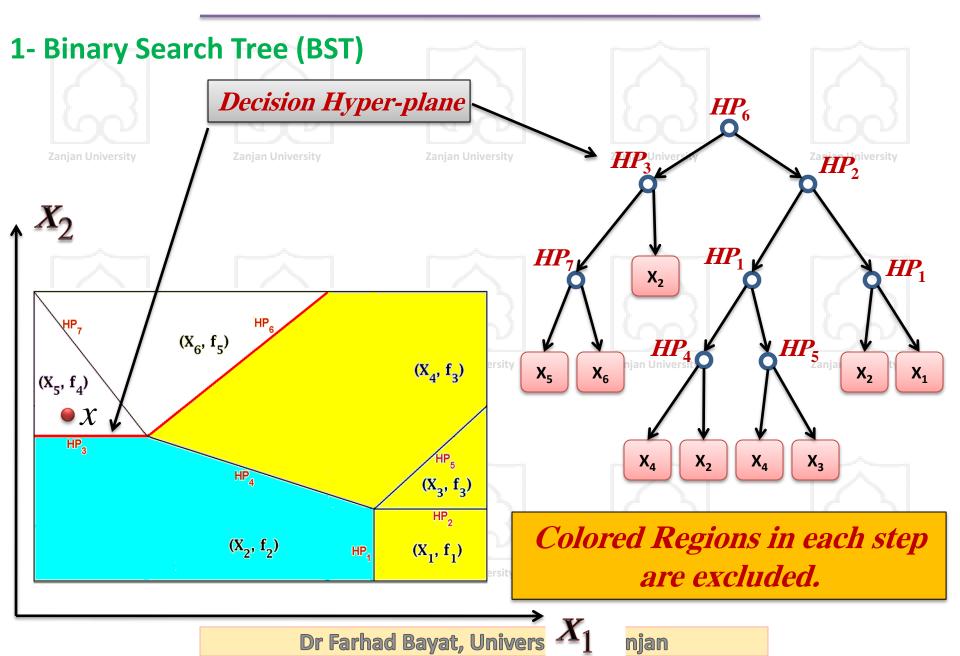
Piecewise Affine Function Evaluation: Point location

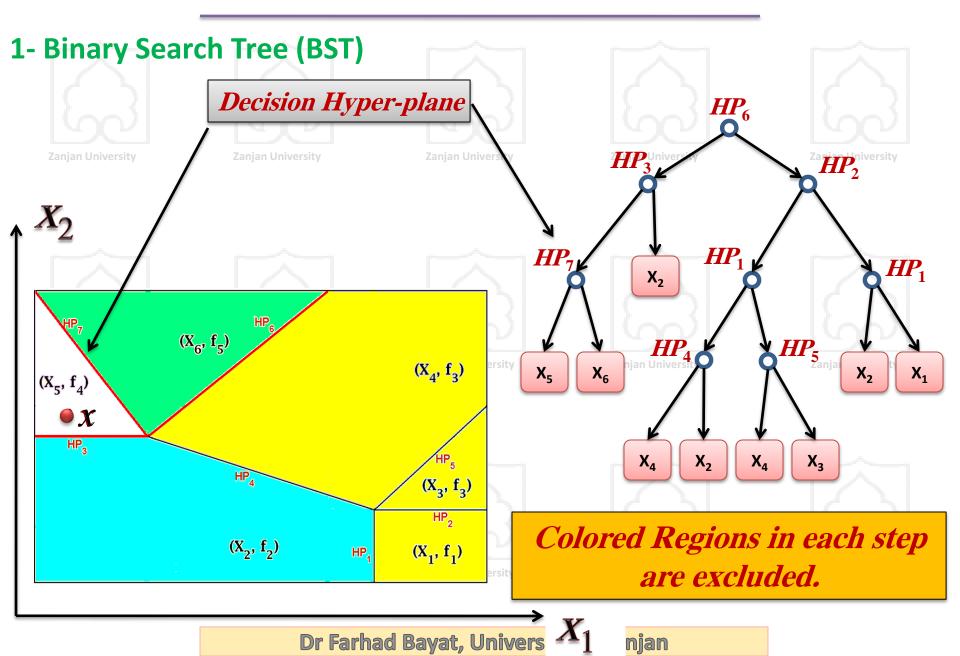


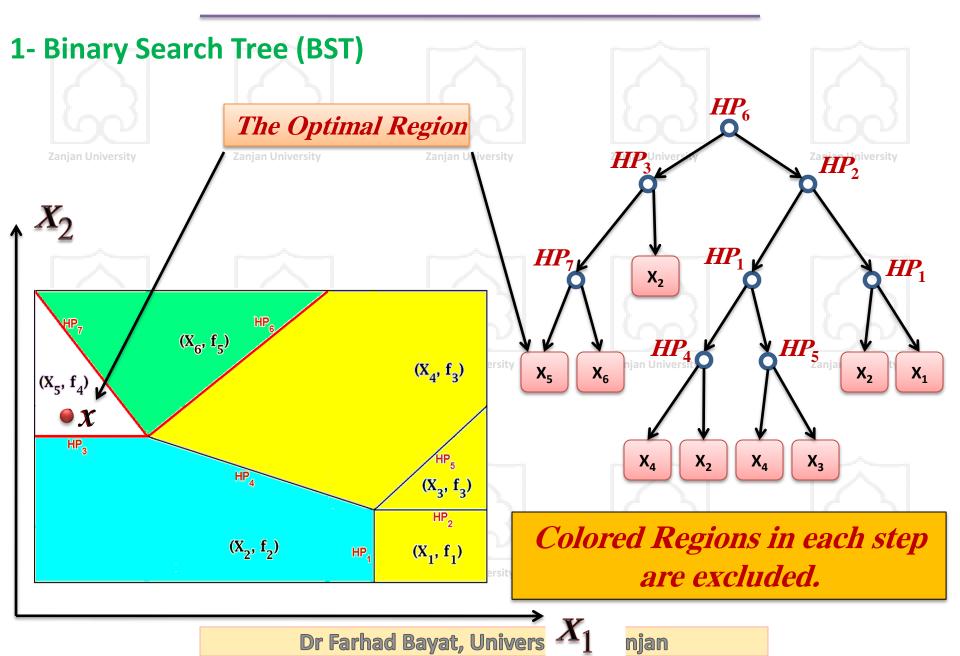
The point location problem











1- Binary Search Tree (BST)



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BUT:

For large number of regions it easily becomes prohibitive regarding
Offline pre-processing time to construct a balanced (close to optimal) tree.

> When the tree expands, the number of nodes increases exponentially (storage).

> There is no flexibility to trade-off between the offline & online complexities, or the use of storage.











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WHAT IS THE MAIN IDEA?

- Using Orthogonal Decision Hyper-planes, rather than the (i) hyperplanes corresponding to the polyhedral regions.
- (ii) **Truncating** the Orthogonal Binary Search Tree and using **Direct Search** (DS) to find optimal solution.





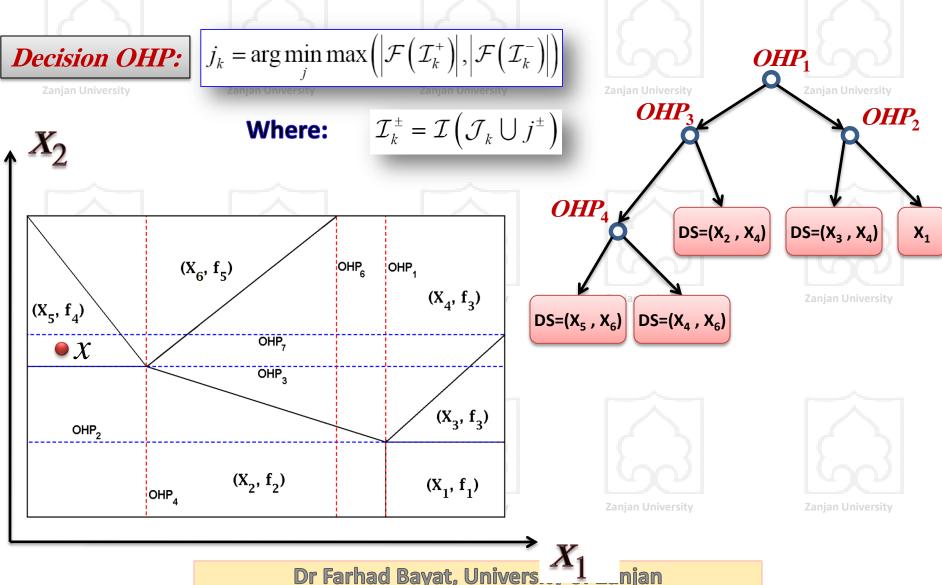


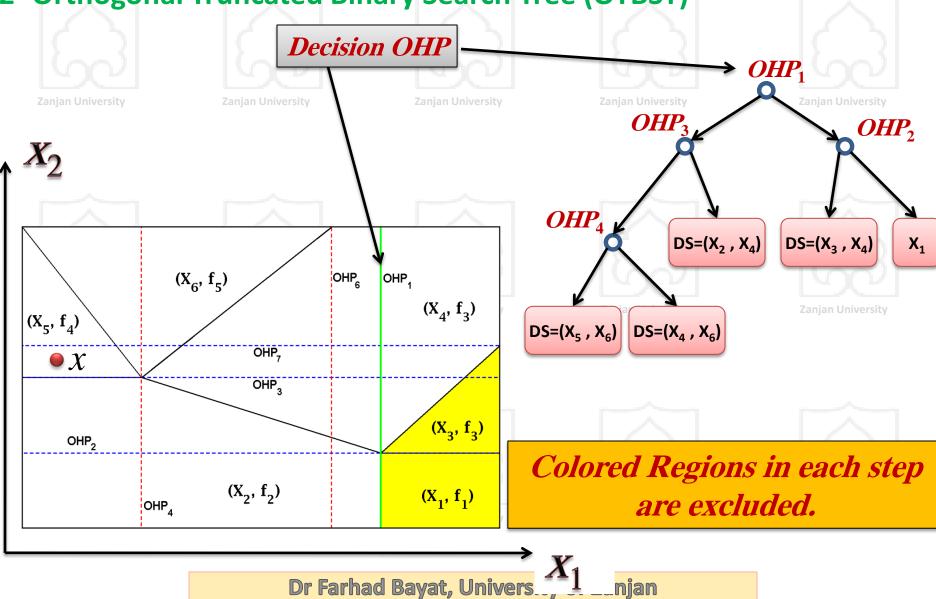


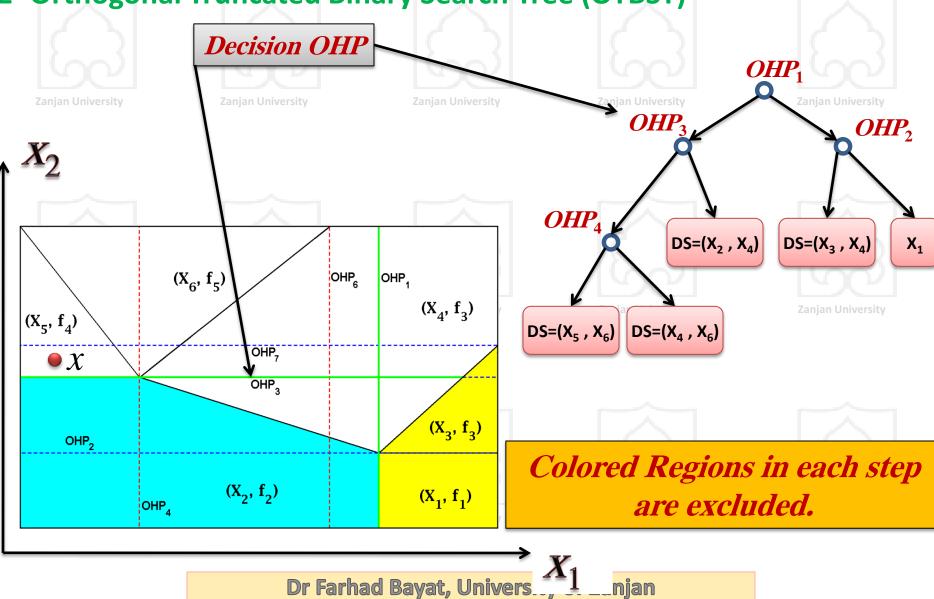


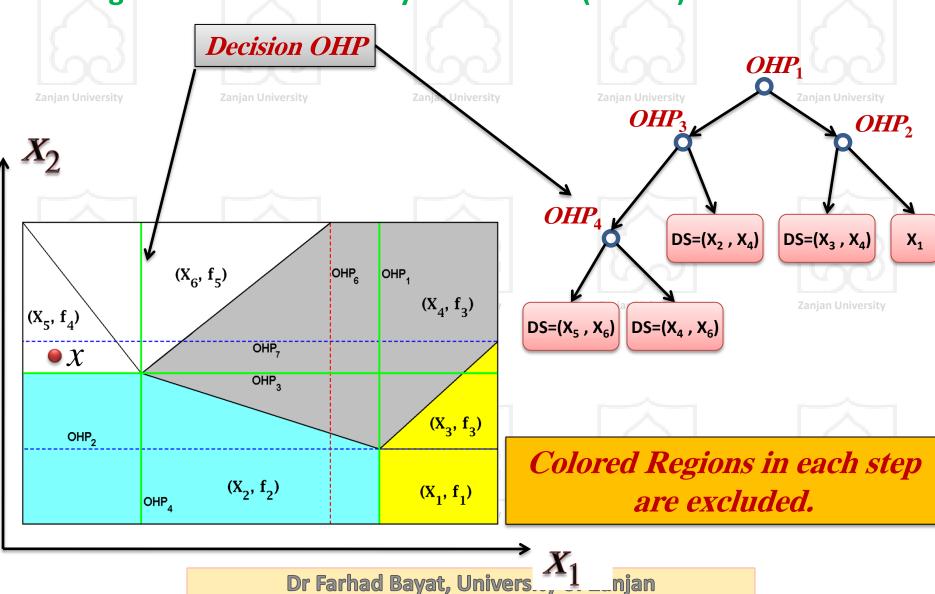


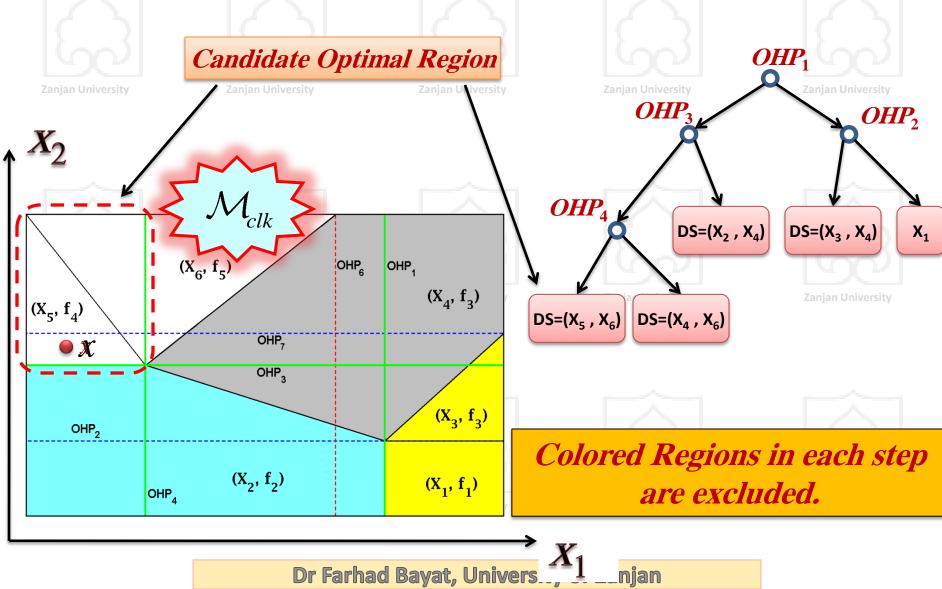
Orthogonal Truncated Binary Search Tree (OTBST)

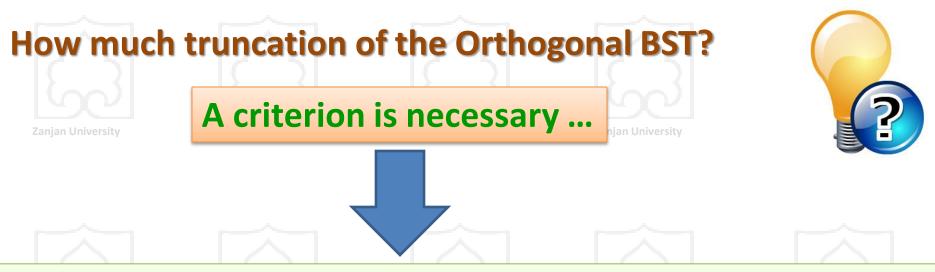








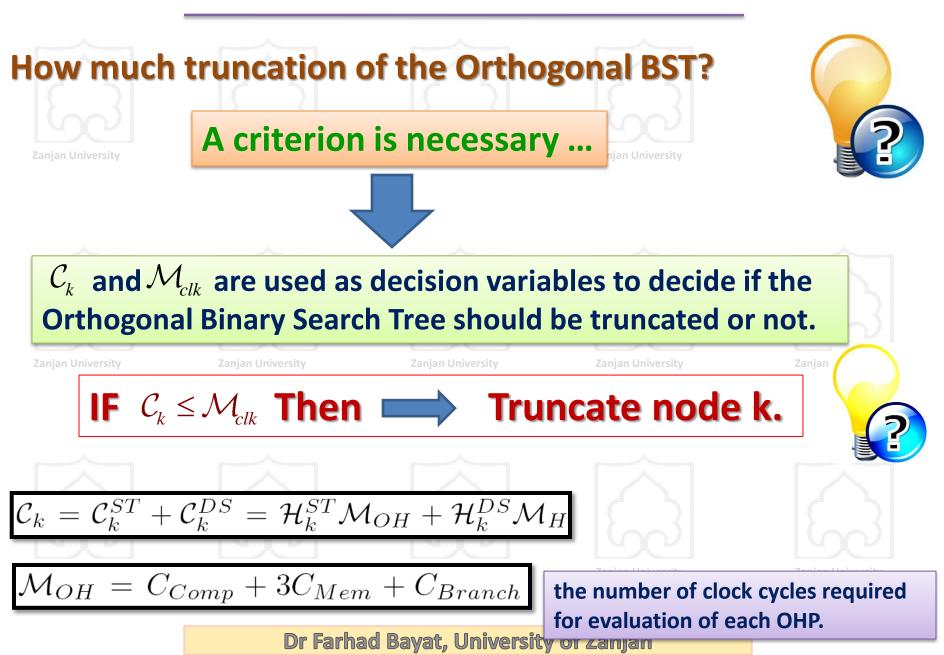




Let \mathcal{M}_{clk} be the maximum admissible number of clock cycles allocated for online piecewise function evaluation in the processor.

we introduce C_k denoting the **number of clock cycles required** to compute the optimal region through the candidate optimal regions using direct search (or any other alternative).

Accordingly,





(i) Fewer discriminating hyperplanes leads to less pre-processing time,

(ii) Simpler regions (hyper-rectangles rather than general polyhedra) gives

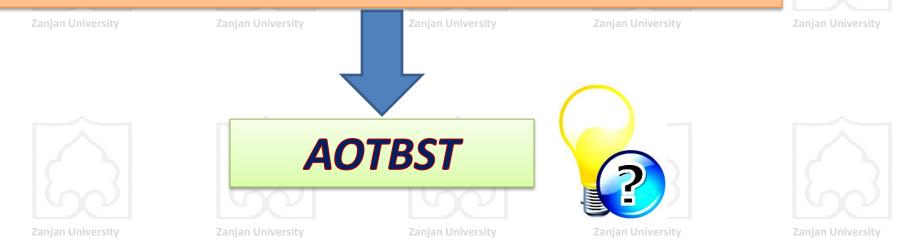
further reduction in the pre-processing demands,

(iii) The OTBST approach enables the designer to trade-off between offline and online complexities, and

(iv) In the OTBST the online evaluation of each node in the search tree is more efficient since the hyper-planes are simpler (orthogonal) in University

Replacing Direct Search (DS) in the OTBST algorithm with an efficient alternative is of interest.

Approximating piece of PWA function defined over each truncated leaf



Main idea:

Assuming eMPC applications with **input** and **soft-state** (output) constraints and continuous solution, with the price of sub-optimality it is possible to avoid storing the whole feasible partition by using a simple approach to approximate the PWA function piece in each **truncated leaf**.

NOTE:

If the piecewise control law is continuous then **relatively small regions** have in practice little influence on the closed-loop stability and performance of the overall system, when perturbing or approximating the control law. Also the existence of measured noise in practice is another factor which makes the relatively small regions less important.

Algorithm: Approximate OTBST (AOTBST)

Offline Procedure

- 1. Let $S = |\mathcal{I}|$ denotes the number of candidate regions, then compute $\mathbf{R} = \{\mathcal{R}_1, ..., \mathcal{R}_S\}, \ \underline{\mathcal{R}_i} = \mathcal{P}(\mathcal{J}) \cap \mathcal{X}_{\mathcal{I}(i)}, \ i = 1, ..., S.$
- Compute and store the Chebyshev center and squared radius for all R_i, i.e. c = {c₁, ..., c_S}, r² = {r₁, ..., r_S}, as presented in Bemporad et al. (2002b). The square is used to avoid online square root operation.
 For i ∈ {1, ..., S}, if 0_{n_x×1} ∈ R_i then set c_i = 0_{n_x×1}.

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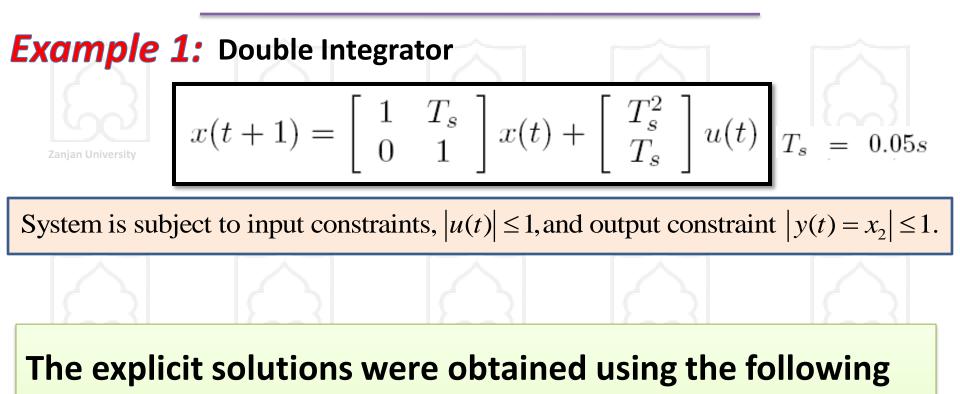
Algorithm: Approximate OTBST (AOTBST)

Online Procedure

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Simulation Results



parameters:

p=2, *Q*=diag([1, 0]), *R* = 1, and *Q*_f = 0 and

for different horizons N=2, 4, 8 and 12.

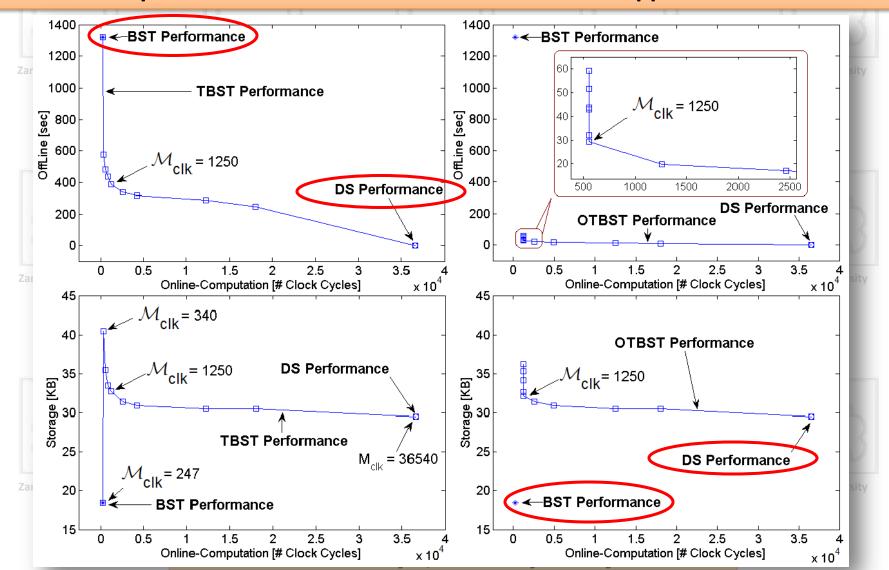
Example 1: Double Integrator

| Γ | | | | | | ~ | - | On | line | |
|----|----|--------|-------|------------------------------|------|-----------|---|-------|-------------------|-------|
| | N | Method | N_r | $\mathcal{M}_{\mathbf{clk}}$ | Nn | Storage | Preprocessing | Clock | -Cycles | |
| | | | | | | (Numbers) | $\operatorname{Time}(\operatorname{sec})$ | Min | Max | |
| ſ | | BST | | | 188 | 752 | 27 | 147 | 187 | ν |
| ar | 2 | TBST | 83 | | 22 | 1096 | 11 | 107 | 513 Ver | ity |
| | | OTBST | | 550 | 33 | 1160 | 3 | 62 | 454 | ſ |
| | | AOTBST | | | | 862 | 3.5 | 62 | 242 | |
| | | BST | 219 | | 421 | 1792 | 160 | 147 | 207 | |
| | 4 | TBST | | 1000 | 33 | 2825 | 62 | 107 | 933 | L |
| | | OTBST | | | 39 | 2890 | 6 | 73 | 952 | Π |
| | | AOTBST | | | | 1779 | 7 | 73 | 474 | - |
| | | BST | | | 1177 | 4736 | 1321 | 187 | 247 $^{ m ver}$ | rsity |
| | 8 | TBST | 627 | 1250 | 84 | 8204 | 514 | 107 | 1227 | L |
| | | OTBST | 027 | | 90 | 8230 | 40 | 62 | 1165 | Γ |
| | | AOTBST | | | | 4070 | 42 | 62 | 619 | |
| Γ | | BST | | | 2181 | 8704 | 5769 | 207 | 267 | 1 |
| | 12 | TBST | 1325 | | 166 | 17202 | 2420 | 127 | 1655 | D |
| | | OTBST | 1525 | 1700 | 131 | 17095 | 118 | 73 | 1531 | P |
| | | AOTBST | | | | 6794 | 121 | 73 | 793 | |

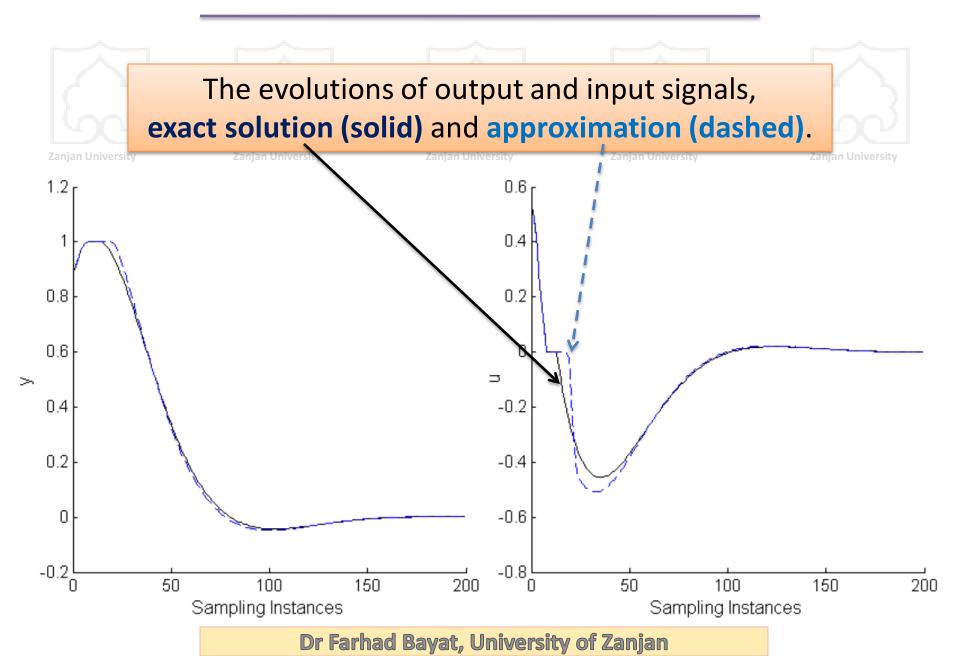
NOTE: All simulations were done based on a low cost processor, e.g. AVR-XMEGA series, with 32 MHz clock frequency.

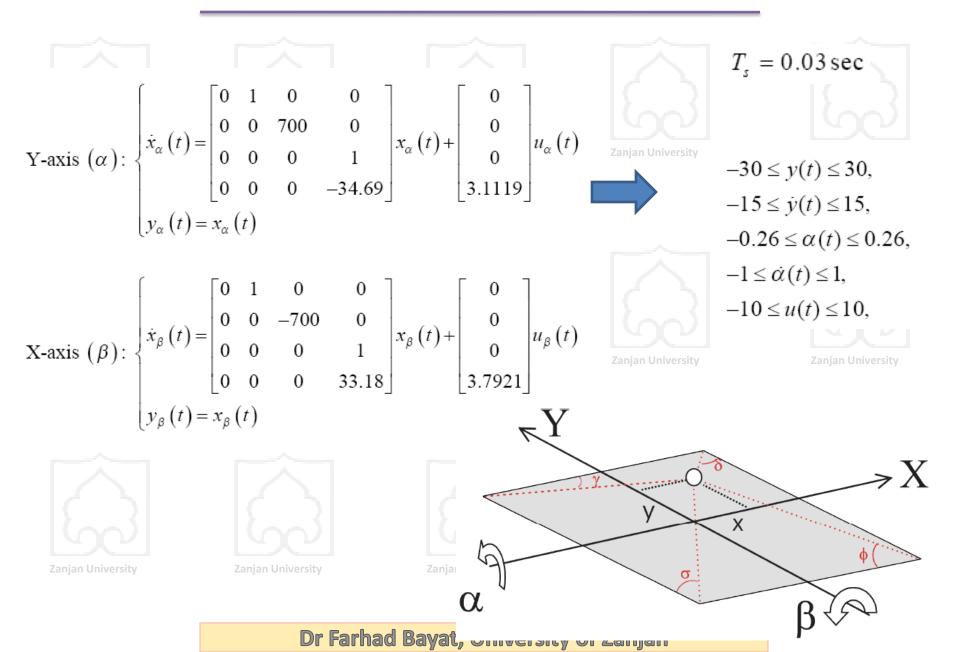
Example 1: Double Integrator, N=8

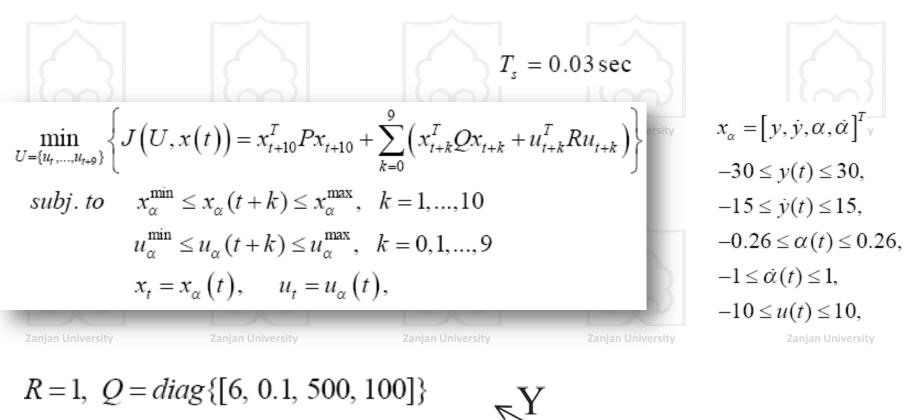
Trade-off characteristics of the TBST and OTBST methods, parameterizing the performance trade-off with the BST and DS approaches.



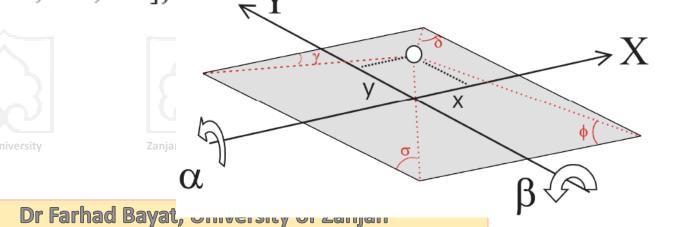
Example 1: Double Integrator, N=8

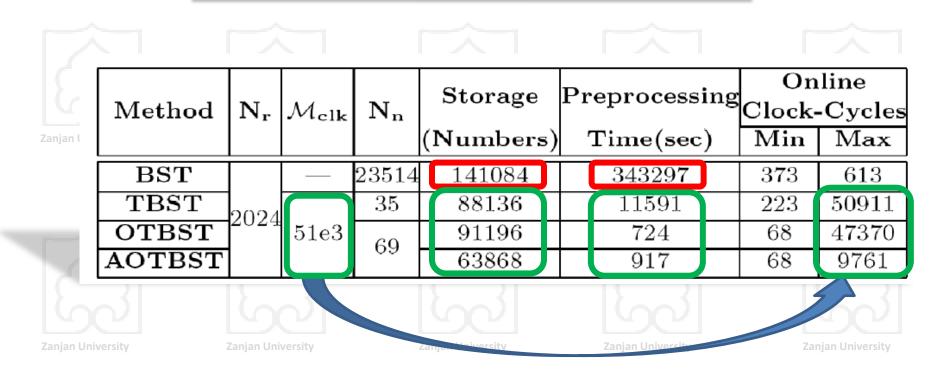






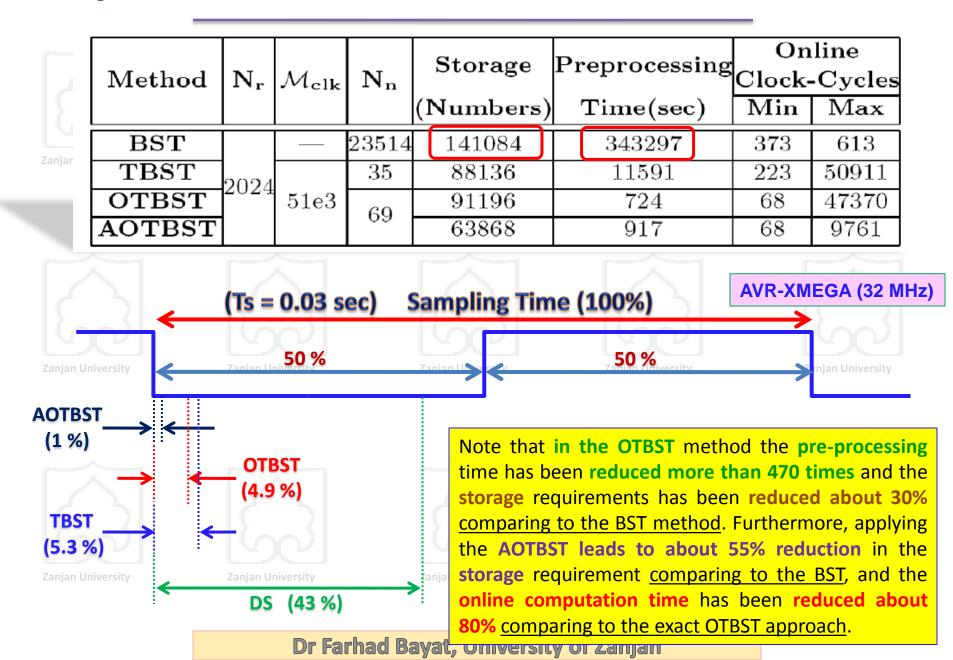
P = Q





NOTE: All simulations were done based on a low cost processor, e.g. AVR-XMEGA series, with 32 MHz clock frequency.





Conclusion

Efficient evaluation of piecewise functions defined over polyhedral partitions was addressed

+ The BST approach is modified by <u>truncating</u> (TBST) and then choosing decision hyper planes among a carefully selected set of <u>axis-orthogonal hyper planes</u> leading to significant <u>reduction in pre-processing time</u> (OTBST).

+ For continuous PWA functions, e.g. explicit MPC application, we proposed to replace the required direct search in the <u>OTBST</u> with a simple function approximation method (<u>AOTBST</u>) leading to significant reduction in the online processing time (with the cost of sub-optimality) comparing to the exact OTBST approach.

+ The AOTBST <u>reduces</u> extensively the <u>storage complexity</u> and <u>offline computation time</u> compared to the <u>BST approach</u>, while <u>guaranteeing the required online computation time</u>.

+ Enables the designer to trade-off between preprocessing time, storage requirement and online computation time.

+ **OTBST is a global approach** that can be applied to **general piecewise nonlinear** (PWNL) functions including **discontinuous** and **overlapping**.

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Thank you for your attention



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